

HW Chap. 3

Thread-level Parallelism

1. In your own words, what is the difference between instruction-level parallelism and thread-level parallelism?
2. Suppose that by using a 2-bit counter for branch prediction (as described in class), the misprediction rate for the `gcc` benchmark approaches 30%; with the branch penalty fixed at 1 cycle. Would you recommend execution on a processor implementing fine-grained multithreading, or coarse-grained multithreading? Justify your answer.